An Optimal Interleaving Scheme with Maximum Time-Frequency Diversity for PLC Systems

Sicong Liu, Fang Yang, Senior Member, IEEE, and Jian Song, Senior Member, IEEE

Abstract—In this paper, an optimal interleaving scheme with maximum time-frequency diversity is proposed to combat against the narrowband interference (NBI) and time-domain impulsive noise (TIN) for orthogonal frequency division multiplexing (OFDM) systems in power line communications (PLC). To improve both the anti-NBI and anti-TIN performance, we propose two criteria based on which the optimized interleaving scheme is designed: (i) to increase the number of different OFDM blocks for one forward error correction (FEC) codeword, which is aimed at NBI mitigation; and (ii) to increase the number of different sub-carriers mapped to the data symbols in one FEC codeword, which is aimed at NBI mitigation. Simulations show that the proposed interleaving scheme can effectively reduce the impairments caused by NBI and/or TIN in OFDM systems under PLC environments, and hence achieve better performance than the conventional block interleaving scheme.

Keywords—time-frequency interleaving, PLC, OFDM, narrowband interference, impulsive noise.

I. INTRODUCTION

Power line communications (PLC) have many advantages over other transmission approaches, such as the prevailing electrical wiring networks, the ubiquitous interfaces to plug in and relatively lower costs, the convenience and efficiency to build up the PLC systems, etc. An increasing number of researches on digital communications in power line channels and its applications for smart grid have arisen recently [1]-[4]. Orthogonal frequency division multiplexing (OFDM) technique is widely adopted in various communication systems, including PLC systems, the digital television terrestrial broadcasting (DTTB) systems [5] [6], etc. The OFDM technique divides the entire bandwidth into many parallel narrow bands which can provide the higher-spectrum efficiency as well as better capability to handle the multi-path effect. There have been many researches on the OFDM technique, such as channel estimation [7], peak-to-average-ratio (PAPR) reduction [8] and synchronization [9]. Specially, a broadband power line communication system using OFDM technique with LDPC code is specified in the power line communication system of ITU G.9960 standard, i.e. the G.hn standard, which has been adopted in many communication systems such as in-home networks and smart grid applications [10].

Although there are advantages for using PLC systems, the channel environment is quite poor. The PLC channel can be modeled by a multi-path channel with frequency-selectivity [11]. There also exist different kinds of attenuation and interferences, such as the narrowband interference (NBI) and the time-domain impulse noise (TIN) in the power line channel. Frequency-selectivity can be well mitigated using the OFDM technique, nevertheless the NBI and TIN in the PLC channel should be specially taken into consideration since they can both cause severe degradation on the system performance, which has drawn much research attention [12] [13].

Interleaving is aimed at providing diversity in time, frequency and/or spatial domain, and has been adopted in the PLC system since the channel conditions of PLC are so poor that more diversity is beneficial to the system performance. Several interleaving schemes to improve the performance of different communication systems have been investigated [14]-[16]. Wang and Kobayashi proposed a method to design an interleaver with practical size for turbo codes [14]. Literature [15] investigated an adaptive interleaved beamforming approach for broadband MIMO/OFDM systems in which beamforming is adaptively interleaved in the spatial domain to achieve performance improvements over conventional adaptive antenna array based OFDM systems in wireless channels. In broadcasting systems, such as the prevailing second generation terrestrial digital video broadcasting standard (DVB-T2), interleaving is adopted to ensure better performance under the severe propagation environment [16]. However, these interleaving methods are not optimized for mitigating the performance degradation due to special impairments, such as TIN and NBI.

For the purpose of reducing the impact of impulse noise in OFDM systems, several methods have been proposed [17] [18]. An interleaving scheme performed post the inverse discrete Fourier transform (IDFT) can achieve better performance under TIN [17]. However, the method is very sensitive to the QAM order and will suffer from remarkable degradation in 64-QAM or higher-order QAM constellations. The performance of the coded OFDM system and the impact of the length of the adopted bit interleaver on coding performance are analyzed in [18]. The aforementioned methods are not specifically designed or optimized for mitigating NBI impairments.

In order to deal with the impairments due to NBI, an OFDM system using convolutional coding with an interleaver designed for channels under narrowband and impulsive noise is described in [19] with bit interleaving rather than symbol interleaving proposed, and the interleaver is specially designed...
for systems using convolutional coding and BPSK modulation only. This constrains its application and performance in other systems, and there is no experimental or simulation results provided in the paper to show the performance.

In this paper, we propose an optimized time-frequency interleaving scheme, which is designed according to two criteria, to combat against NBI and TIN in the PLC system. The two proposed criteria based on which the optimized interleaver is designed can remarkably improve time and frequency diversity. The first criterion is met by optimizing the distribution of OFDM blocks in the FEC codewords, while the second criterion is satisfied with the optimized distribution of sub-carriers in the FEC codewords. Hence, block interleaving with optimized block size is utilized as time interleaving, while the novel frequency interleaving method realized by the proposed row shifting in sub-matrix operation is performed. Furthermore, the proposed interleaving process is at the symbol level (i.e. data symbol level) rather than the bit level, which leads to more effectiveness and low implementation complexity.

The rest of this paper is organized as follows: The characteristics of the system model for which the interleaver is designed are described in Section II. The statistical models of the NBI and TIN in the PLC channel are given in Section III. The optimized interleaving scheme along with the two criteria for designing the optimized interleaver for PLC systems is derived in Section IV. The paper concludes with a quantitative analysis for the interleaving schemes and simulation results in Section V, and a summary.

II. SYSTEM MODEL WITH TIME-FREQUENCY INTERLEAVING

The OFDM system model used for our analysis is depicted in Fig. 1. The source bit stream \( \{b_k\} \) is passing through the encoder and generates forward error correction (FEC) codewords \( \{e_i\} \) with the length of \( L_0 \). Bit-to-symbol mapping is performed by \( M \)-QAM constellation, which produces complex data symbols \( \{c_i\} \). The data symbols are then interleaved in the block interleaver, which is adopted as a time and/or frequency interleaver, with \( I_R \) rows and \( I_C \) columns. Afterwards, the pilots are inserted, and combined with the interleaved data symbols to form the OFDM block in the frequency domain. The number of data sub-carriers in an OFDM block is \( N \). The frequency interleaving is performed before the OFDM block data is processed by the IDFT module to obtain the time-domain OFDM symbol \( x_m \). The OFDM symbols are then transmitted over the PLC channel deteriorated by NBI and TIN. The received signal \( \tilde{x}_m \) can be expressed as

\[
\tilde{x}_m = x_m + w + n_{NBI} + I_{TIN}
\]

where \( w \) is the background noise in the PLC channel modeled as additive white Gaussian noise (AWGN) with zero mean and variance \( \sigma^2_w \), and \( n_{NBI} \) and \( I_{TIN} \) denote NBI and TIN, respectively.

Finally, the receiver performs the inverse operations of the transmitter in a reversed order, and obtains the transmitted bits \( \{b_k\} \).

III. STATISTICAL MODELS OF THE NBI AND THE TIN

We discuss the state-of-the-art statistical models for the NBI and TIN in PLC systems in this section. The proposed time-frequency interleaving approach is non-parametric, but the PLC transmission environments with the NBI and the TIN can be simulated and the effectiveness of the proposed approach can be verified through these statistical models.

A. Statistical NBI Model in PLC

The statistical NBI signal model in the frequency-domain can be denoted as the vector of \( \hat{e} = [e_0, e_1, \cdots, e_{N-1}]^T \) of length \( N \) with only few nonzero entries. The support of the NBI \( \Gamma \) is defined as the set of the positions of the nonzero entries, and is given by \( \Gamma = \{k | e_k \neq 0 , k = 0, 1, \cdots, N - 1 \} \). For the NBI signal model adopted in this paper, the positions of the nonzero entries are randomly distributed in all the data sub-carriers. The distribution of the support of the NBI is stochastic, which is also adopted and verified by existing NBI models [20] [21]. Without loss of generality, this NBI model is a general case for many situations, including the practical situation in which multiple NBI nonzero entries are clustered in some interferer groups. The sparsity level \( K \) of the NBI is defined as the cardinality of the support, i.e. \( K = |\Gamma| \). The interference-to-noise ratio (INR) is defined as \( P_e / \sigma^2_w \), where \( P_e = \sum_{k \in \Gamma} |e_k|^2 / K \) denotes the average power of the NBI. INR is an indicator of the NBI intensity compared with the background noise. The intensity of NBI is also described by the signal-to-interference ratio (SIR), which is given by

\[
SIR = \frac{E\{|x_m|^2\}}{P_e}
\]

Since the sparsity level of the NBI signal model is variable and the distribution of the support of the NBI is random, the NBI model in this paper is more general and more practical for PLC systems compared with the NBI models in [20] and [21] where only one nonzero entry, i.e. only one sinusoidal component, is considered.
B. Statistical TIN Model in PLC

The statistical properties of the instantaneous amplitude and the random emissions of the TIN in the PLC systems have been modeled in literature empirically, mainly including the burst block-sparse model [22], the Gaussian mixture model [23] and the Middleton’s Class A model [24].

1) Burst Block-Sparse Model: In practical PLC systems, TIN occurs in bursts and corrupt blocks of received OFDM signal samples [22]. The burst block-sparse model in [22] assumes that only a small portion of the received samples are corrupted by TIN, and the corrupted samples are grouped in burst blocks. Hence, the occurrence of TIN bursts is in-between the extreme cases of i.i.d. TIN and long TIN bursts that destroy entire OFDM symbol [22][31]. The burst block-sparse model is given by

\[ i_{TIN} = \begin{bmatrix} i_1 \\ \vdots \\ i_D \\ \vdots \\ i_{N-D+1} \\ \vdots \\ i_N \end{bmatrix} \]  
(3)

where the block-size of bursts is \( D \) and only \( q \ll p \) vectors \( i_j \) have non-zero Euclidean norm [22].

The probability of \( \Lambda \) which is the number of TIN bursts per second follows Poisson process [25], and is given by \( P(\Lambda) = \Lambda^\lambda e^{-\lambda}/\lambda! \), where \( \lambda \) denotes the rate of TIN arrivals.

2) Gaussian Mixture Model: The instantaneous amplitude of the time-domain asynchronous impulsive noise in the PLC network can be modeled by the Gaussian mixture distribution [23], with the probability density distribution (pdf) given by \( p_Z(z) = \sum_{j=0}^{m} \beta_j \cdot g_j(z) \), where \( g_j(z) \) is a Gaussian pdf with zero mean and variance of \( \sigma_j^2 \), and \( \beta_j \) is the mixture coefficient of the corresponding Gaussian pdf.

3) Middleton’s Class A Model: A common statistical model of the TIN instantaneous amplitude is the Middleton’s Class A model with the parameters of the overlapping factor \( A \) and the background-to-impulsive-noise power ratio \( \Omega \) [24]. Gaussian mixture distribution can generate the special case of Middleton’s Class A distribution when the parameters \( \beta_j = e^{-A/2}\Omega/\sqrt{2\pi}\Gamma^{-1/2}(1+1/\Omega) \) and \( \sigma_j^2 = \frac{\Omega}{\sqrt{4\pi}}J_{m}\rightarrow\infty \) as \( J_m \rightarrow \infty \). The Middleton’s Class A model is widely adopted in analytical research and simulations for PLC systems [26][27].

IV. TIME-FREQUENCY INTERLEAVING METHOD
A. Conventional Interleaving Methods

As for the conventional block interleaving scheme, data symbols are written row-wise into the block interleaver with \( I_R \) rows and \( I_C \) columns, and then read column-wise out to form the OFDM blocks. The conventional block interleaving is time interleaving, aiming at mitigation of TIN. Time-domain burst errors could be dispersed into several FEC codewords to reduce the probability of decoding errors by block interleaving.

It is crucial to choose an appropriate block size for a block interleaver to achieve better anti-TIN capability for a certain system. The row number \( I_R \) is usually referred to as the interleaving depth, while the column number \( I_C \) is the interleaving width. If the block size is not specially designed for OFDM systems in the presence of TIN, the performance will degrade greatly. Meanwhile, in the presence of NBI, the conventional block interleaving scheme may have a serious drawback in sub-carrier allocation of LDPC codewords.

Another conventional interleaver, the random bit interleaver, distributes the bits among several codewords into different time slots and sub-carriers. The design of a bit interleaver is also aimed at providing larger time/frequency diversity, which is a fundamental idea of the proposed scheme in this paper. However, in the presence of TIN and NBI, a good random bit interleaver is very difficult to design in practice, and one bit interleaver is not robust and is likely to degrade in different constellations, interleaving depths or transmission modes.

B. Proposed Interleaving Method

In this work, we propose two criteria for improving and maximizing the time diversity and frequency diversity, respectively, and the optimized interleaver is designed to combat against both TIN and NBI according to the two criteria.

Firstly, for better anti-TIN capability, we propose Criterion 1 as the criterion for choosing the optimized block size for the block interleaver as time interleaving. Under this criterion, the time diversity of the PLC system is maximized.

**Criterion 1.** The Maximum Independent OFDM Block (MIOB) Criterion: to increase the number of different independent OFDM blocks for one FEC codeword.

Since the TIN bursts occur in groups and will ruin the data symbols of the OFDM block when they occur, the OFDM blocks should be average distributed in the codewords. If the allocation of OFDM blocks for one codeword is not average, e.g. the number of different OFDM blocks for a specific codeword is smaller and the number of data symbols of the TIN deteriorated OFDM block is larger on average, the impairments caused by TIN are more concentrated in this codeword, resulting in higher probability of decoding errors.

Therefore, the number of different OFDM blocks for one FEC codeword, i.e. the parameter of independent OFDM block (IOB) number \( N_{IOB} \), should be increased to improve the anti-TIN capability. With the increase of IOBs mapped to each codeword, the possibility of unsuccessful decoding due to the concentration of TIN bursts is decreased. Moreover, we propose a time-domain merit factor \( \xi_T \) as a general indicator of \( N_{IOB} \) in one codeword to measure the anti-TIN capability quantitatively as

\[ \xi_T = \frac{1}{L_{C}} \sum_{n=0}^{L_{C}-1} \min \left\{ \frac{N_{IOB}(n)}{I_R \cdot I_C \cdot L_{sym} / N} \right\} \]  
(4)

where \( L_{sym} \), \( L_{C} \) and \( N_{IOB}(n) \) denote the number of data symbols in each LDPC codeword, the number of LDPC codewords in the interleaver, and the number of IOBs in the \( n \)th LDPC codeword respectively. Hence \( \xi_T \) is the average of the ratio of \( N_{IOB}(n) \) to its maximum value \( \min \{ I_R \cdot I_C / N, L_{sym} \} \).

To satisfy Criterion 1, the design of the block size can be concluded as an optimization problem as
To optimize (5), intuitively we should make sure that each LDPC codeword occupies as many IOBs as possible. After the optimization of (5), we can obtain the optimized interleaving block size design as given by,

\[
I_C = \frac{L_b}{p \cdot \log_2 (M)} = \frac{L_{sym}}{p} \tag{6}
\]

\[
I_R = \delta \cdot \mathcal{L} \{N, L_{sym}\}/I_C \tag{7}
\]

where \(\mathcal{L} \{\cdot, \cdot\}\) is the least common multiple operator, and \(L_b\) denotes the number of bits in a codeword. The parameter \(p\) is optional and is usually set to a small prime number, such as 2, 3, 5, etc. Eq. (7) ensures that the proposed interleaver contains integer number of OFDM blocks and LDPC codes. The total interleaving delay is measured by the number of data symbols in the interleaver given by \(I_R \cdot I_C = \delta \cdot \mathcal{L} \{N, L_{sym}\}\), where \(\delta\) is an integer that can be adjusted according to the acceptable transmission delay.

With the proposed block size in (6) and (7), the parameter of \(N_{IOB}\) and the anti-TIN capability of the block interleaver will be optimized. Equation (6) assures that each LDPC codeword occupies exactly \(p\) rows in the interleaver, which makes it possible for each codeword to be mapped to all the OFDM blocks without loss. Meanwhile, from (7) it can be deduced that each OFDM block occupies one column if \(N \geq I_R\), which is equivalent to

\[
\mathcal{G} \{L_{sym}, N\} \geq p \tag{8}
\]

where \(\mathcal{G} \{\cdot, \cdot\}\) is the greatest common divisor operator. Recall that we have set \(p\) as a small prime number. The reason is that when \(p\) is set as a small prime number, the parameter \(L_{sym}\) and \(N\) specified in G.hn or other OFDM systems are large enough to contain \(p\) as a common divisor. In this condition, it is easy to meet (8).

Hence each LDPC codeword will contain all the OFDM blocks in the interleaver, which is the optimized case against TIN and is reached based on Criterion 1. However, with conventional block size, each LDPC codeword is much likely to occupy less than one row, or each OFDM block is much likely to occupy less than one column. This means \(N_{IOB}\) of the LDPC codewords are smaller, leading to poorer performance under TIN.

Afterwards, Criterion 2 is proposed as the criterion to design the optimized frequency interleaving scheme for better anti-NBI capability, with the constraint of Criterion 1 to ensure anti-TIN capability. Hence under both the two criteria, both the time and frequency diversity of the PLC system can be maximized.

**Criterion 2. The Maximum Independent Sub-Carrier (MISC) Criterion:** to increase the number of different independent OFDM sub-carriers mapped to the data symbols in one FEC codeword.

The reason to set up Criterion 2 is similar to that of Criterion 1. If the allocation of sub-carriers of one codeword is uneven, e.g. the number of different sub-carriers for a specific codeword is smaller and the number of data symbols mapped to the NBI impacted sub-carrier is larger on average, the impairments caused by NBI are more concentrated in this codeword, leading to the decoding errors even though the average signal-to-noise ratio is high enough to decode other codewords correctly.

Hence the number of different sub-carriers mapped to the data symbols of each codeword, i.e., the parameter of independent sub-carrier (ISC) number \(N_{ISC}\), should be increased in order to distribute the NBI-contaminated sub-carriers in more codewords. Allocation of sub-carriers of the data symbols in one FEC codeword is better scattered and more average when \(N_{ISC}\) is larger, which would lead to more robust anti-NBI performance. To quantitatively evaluate the anti-NBI capability of different interleaving schemes and facilitate the frequency interleaving design based on Criterion 2, we propose another frequency-domain merit factor \(\xi_F\) as a general indicator of \(N_{ISC}\) in one codeword, which is given by

\[
\xi_F = \frac{1}{L_C} \sum_{n=0}^{L_C-1} \frac{N_{ISC}(n)}{\min\{N, L_{sym}\}} \tag{9}
\]

where \(N_{ISC}(n)\) is the number of ISCs of the \(n\)th LDPC codeword. \(\xi_F\) is the average of the ratio of \(N_{ISC}(n)\) to its maximum value \(\min\{N, L_{sym}\}\), which can be achieved under ideally average sub-carrier distribution.

The design of the frequency interleaving scheme to meet Criterion 2 can be also concluded as an optimization problem described by,

\[
\text{opt : max } \left\{ \xi_F \mid I_C = \frac{L_{sym}}{p}, I_R = \frac{\delta \cdot \mathcal{L} \{N, L_{sym}\}}{I_C} \right\} \tag{10}
\]

The constraints in (10) are set by (6) and (7) to ensure that the anti-TIN capability is not affected during the optimization process of \(\xi_F\). To solve the problem described in (10), from an intuitive perspective of view, we need to make sure that the data symbols of each LDPC codeword are mapped to as many ISCs as possible. Hence, we propose a novel frequency interleaving scheme using block interleaver as shown in Fig. 2. Firstly, data symbols are written row-wise into the block interleaver. The interleaving matrix is divided into several sub-matrices by column, each having the same number of columns. The number of sub-matrices \(S\) is optional and can be any divisor of \(I_c\). The basic concept of the proposed interleaving scheme is to perform a novel row shifting in sub-matrix operation in the block interleaver. Assuming that \(C^{(r)}\) denotes the \(r\)th sub-matrix and \(C = [C^{(0)}, C^{(1)}, \cdots, C^{(S-1)}]\) represents the block matrix, the row shifting in sub-matrix operation is to shift the rows of sub-matrix \(C^{(r)}(r = 0, 1, \cdots, S-1)\) cyclically down by \(f_r\) rows to produce the shifted sub-matrix \(C^{(r)}_{f_r}\), which is expressed by
\[
\mathbf{c}^{(r)}_i = \mathbf{c}^{(r)}_j, j = (i + I_R - f_r) \mod I_R, 0 \leq i, j < I_R
\]  
where \( \mathbf{c}^{(r)}_i \) and \( \mathbf{c}^{(r)}_j \) denote the \( j \)th and \( i \)th rows of \( \mathbf{C}^{(r)} \) and \( \overline{\mathbf{C}}^{(r)} \), respectively. The numbers of rows to be shifted for each sub-matrix can be any integer. To simplify the calculation, \( f_r \) can be given by

\[
f_r = p \cdot r, 0 \leq r < S
\]

where \( p \) is decided by (6). After the row shifting in sub-matrix operation, the derived block matrix \( \mathbf{C} \) is read column-wise out to form OFDM blocks.

Let us compare the anti-NBI capabilities between the conventional block interleaving scheme and the proposed frequency interleaving scheme. For the conventional scheme, we define every \( \mathcal{Z} \{I_R, N\} \) data symbols in the block interleaver as a "cyclic unit", which contains integer number of columns and OFDM blocks. Data symbols of each cyclic unit would be mapped to the sub-carriers with the same pattern repeatedly, since the mapping patterns of all cyclic units are the same. Hence the allocation of sub-carriers to a specific codeword in the other cyclic units is the same as in the first one, which provides no contribution to \( N_{ICS} \).

However, in the proposed scheme, since each cyclic unit can be treated as a sub-matrix and is shifted down by different number of rows defined by (12) before forming OFDM blocks, patterns of sub-carriers mapped to the data symbols in a specific codeword within different sub-matrices are different and not repeated. From these analyses and (12), the proposed scheme can achieve the optimized \( \xi_F \) for (10) obviously. Therefore, \( N_{ICS} \) of a codeword in the proposed scheme is much larger than that in the conventional scheme, leading to a better anti-NBI capability. Since the row shifting in sub-matrix operation has the effect of scattering the patterns of data symbols mapping to sub-carriers, the operation can be regarded as frequency interleaving. Consequently the proposed scheme is a time-frequency interleaving.

According to the two proposed criteria, the optimal time-frequency interleaver is designed with largest time and frequency diversity. This goal is achieved through the two optimization problems aiming at combat against TIN and NBI, respectively. By utilizing both the time and frequency interleaving, we can improve the anti-NBI and anti-TIN capabilities of OFDM systems, such as G.hn system using LDPC coding studied in this paper. Experimental simulations are carried out in different channel interference environments with the parameters specified in G.hn standard to show the performance improvements of the proposed scheme in the next section.

V. EVALUATION AND SIMULATION RESULTS

A. Interleaving Performance Evaluation

The computation of the merit factors of the proposed Criterion 1, the proposed Criterion 1&2, the conventional block interleaving and random bit interleaving schemes are listed in Table I with LDPC length \( L_b = 8640 \) and data sub-carrier number \( N = 256 \), which is specified in the PLC G.hn system [10]. The column and row numbers of the interleaver are also given in Table I, which shows that the conventional block interleaving and random interleaving schemes have the same interleaving delay with that of the proposed scheme in both 16QAM and 64QAM modulations. The interleaving delay for the proposed scheme and the benchmarks are fixed for effective comparison. It is noticed that the proposed Criterion 1 scheme is much larger than that of the conventional block and random interleaving schemes, and \( \xi_F \) of the proposed Criterion 1&2 scheme is also significantly larger than those of the conventional block and random interleaving schemes in both QAM modes. It is quantitatively indicated that the anti-TIN and anti-NBI capabilities of the proposed schemes meeting Criterion 1 and Criterion 2 are remarkably better than those of its counterparts. According to the merit factors \( \xi_T \) and \( \xi_F \), both the maximum time and frequency diversities are achieved using the proposed Criterion 1&2 interleaving scheme since both \( \xi_T \) and \( \xi_F \) reach their maximum theoretical value of one. Due to these analyses, it is verified that the maximum time-frequency diversity can be achieved.

<table>
<thead>
<tr>
<th>TABLE I</th>
<th>EVALUATION WITH MERIT FACTORS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Schemes</td>
<td>QAM Order</td>
</tr>
<tr>
<td>S1. Criterion 1&amp;2</td>
<td>16</td>
</tr>
<tr>
<td>S2. Criterion 1</td>
<td>16</td>
</tr>
<tr>
<td>S3. Block ITLV</td>
<td>16</td>
</tr>
<tr>
<td>S4. Random ITLV</td>
<td>16</td>
</tr>
<tr>
<td>S1. Criterion 1&amp;2</td>
<td>64</td>
</tr>
<tr>
<td>S2. Criterion 1</td>
<td>64</td>
</tr>
<tr>
<td>S3. Block ITLV</td>
<td>64</td>
</tr>
<tr>
<td>S4. Random ITLV</td>
<td>64</td>
</tr>
</tbody>
</table>

Furthermore, it can be verified the performance is guaranteed or improved under multi-path fading channels. The performance against multi-path fading can be quantitatively measured by the “duo-distance”, which is commonly used in literature [14]. The duo-distance can be defined as the interleaving duo-distance \( d_1 \) and the de-interleaving duo-distance \( d_2 \). The interleaving duo-distance \( d_1 \) of a pair of
adjacent data symbols at the input of the interleaver is defined as the distance of these two data symbols at the output of the interleaver. Similarly, the de-interleaving duo-distance \( d_2 \) of a pair of adjacent data symbols at the output of the interleaver is defined as the distance between these two data symbols at the input of the interleaver. The duo-distances \( d_1 \) and \( d_2 \) should be made as large as possible in order to lower the correlation between the interleaver input sequence and the output sequence, and this will disperse the data symbols of one codeword into scattered frequencies and scattered time slots, leading to better performance under time-selective and frequency-selective fading channels.

### TABLE II

**Duo-Distances of the Interleaving Schemes**

<table>
<thead>
<tr>
<th>( I_R )</th>
<th>( I_C )</th>
<th>QAM Order</th>
<th>Proposed ITLV</th>
<th>Block ITLV</th>
</tr>
</thead>
<tbody>
<tr>
<td>240</td>
<td>432</td>
<td>16</td>
<td>( d_1 = 477.89 )</td>
<td>( d_1 = 289.76 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>( d_{1,\text{min}} = 240 )</td>
<td>( d_{1,\text{min}} = 240 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>( d_2 = 859.74 )</td>
<td>( d_2 = 578.31 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>( d_{2,\text{min}} = 432 )</td>
<td>( d_{2,\text{min}} = 432 )</td>
</tr>
<tr>
<td>120</td>
<td>288</td>
<td>64</td>
<td>( d_1 = 238.18 )</td>
<td>( d_1 = 127.92 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>( d_{1,\text{min}} = 120 )</td>
<td>( d_{1,\text{min}} = 120 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>( d_2 = 570.43 )</td>
<td>( d_2 = 351.66 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>( d_{2,\text{min}} = 288 )</td>
<td>( d_{2,\text{min}} = 288 )</td>
</tr>
</tbody>
</table>

The duo-distances \( d_1 \) and \( d_2 \) of the proposed **Criterion 1&2** interleaving scheme and the conventional block interleaving scheme are given in Table II. It is noted that the average duo-distances \( d_1 \) and \( d_2 \) of the proposed interleaving scheme are both significantly larger than those of the conventional block interleaving scheme, and the minimum duo-distances \( d_{1,\text{min}} \) and \( d_{2,\text{min}} \) are the same, which explicitly indicates that the data symbols of the proposed interleaving scheme are thoroughly dispersed in both time and frequency dimensions. Hence the performance of the proposed interleaving scheme can be guaranteed and even better than the conventional block interleaving under multi-path fading channels.

Based on the fact that the merit factors \( \xi_T \) and \( \xi_F \) are optimized and the large duo-distances \( d_1 \) and \( d_2 \) are guaranteed at the same time, it can be concluded theoretically that the proposed **Criterion 1&2** interleaving scheme achieves the optimal anti-NBI and anti-TIN capabilities and the maximum time-frequency diversity under both AWGN and multi-path fading channels, with guaranteed interleaving performance under frequency-selective channels.

### B. Simulation Results of BER Performance

Simulations of the proposed **Criterion 1**, **Criterion 1&2** schemes and the conventional block interleaving and random bit interleaving schemes with the same interleaving delay are performed in the G.hn system, and relevant simulation parameters are listed in Table III. \( \delta = 3 \) is used to fix the interleaving delay for the proposed scheme and the benchmarks. The AWGN channel and a multi-path PLC channel model [11] with NBI and TIN are adopted to evaluate the effectiveness of the schemes. The detailed parameters of the PLC multi-path channel are given in [11]. Non-ideal channel estimation is used at the receiver in the simulations. The SIR and sparsity level \( K \) of the NBI for different QAM orders are given in Table III. All the \( N \) data sub-carriers are used to transmit data in simulations. As is described in Section III, the TIN occurrence has the burst block-sparse property [22], and the arrival rate of the TIN bursts is described by a Poisson point process with a medium value of \( \lambda = 50 \text{ sec}^{-1} \) [28]. The instantaneous amplitude of the TIN is modeled by the Middleton’s Class A distribution with the parameters of \( A \) and \( \Omega \) that are also given in Table III.

### TABLE III

**Simulation Parameters**

<table>
<thead>
<tr>
<th>Schemes</th>
<th>( I_R )</th>
<th>( I_C )</th>
<th>QAM Order</th>
<th>NBI Parameters</th>
<th>TIN Parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td>Proposed</td>
<td>240</td>
<td>432</td>
<td>16</td>
<td>( K = 5 )</td>
<td>( A = 0.15 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SIR = -2.9 dB</td>
<td>( \Omega = 0.02 )</td>
</tr>
<tr>
<td>Block ITLV</td>
<td>480</td>
<td>216</td>
<td>64</td>
<td>( K = 4 )</td>
<td>( A = 0.1 )</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SIR = -2.7 dB</td>
<td>( \Omega = 0.01 )</td>
</tr>
<tr>
<td>Random ITLV</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

BER performances of different interleaving schemes under the AWGN channel with different interferences are depicted in Figs. 3, 4 and 5, respectively. It can be noted from Fig. 3 that with both NBI and TIN, the proposed two schemes can achieve more than 2 dB and 1 dB gain compared to those of block interleaving and random interleaving, respectively, under the AWGN channel in both 16QAM and 64QAM. Results in Fig. 3 indicate that the proposed two criteria and the optimized interleaver designed according to them have remarkable advantages over the counterparts.

In Fig. 4, it is shown that under AWGN channel with only NBI, the proposed **Criterion 1&2** scheme is the best among the three schemes, with more than 1 dB gain in 16QAM and 0.7dB gain in 64QAM. This verifies the effectiveness of **Criterion 2** and the row shifting in sub-matrix operation in improving the anti-NBI capability, since the other two schemes are not designed by **Criterion 2**. From the theoretical analysis in Section IV and the quantitative analysis in Table I, \( \xi_F \) of the proposed **Criterion 1&2** scheme is the best among all the three schemes indicating the best anti-NBI capability, which is verified by the results in Fig. 4.

The anti-TIN capability is also verified from Fig. 5, where both the proposed **Criterion 1** and the proposed **Criterion 1&2** schemes achieve more than 1dB gain at the BER of \( 10^{-5} \) in 64QAM, and 0.3dB gain in 16QAM compared with that of conventional block interleaving. Since \( \xi_T \) of conventional block interleaving is the poorest in Table I, it is proved that the anti-TIN capability is greatly improved by designing the interleaver block size according to **Criterion 1**.

The BER performances under the PLC multi-path channel are also depicted in Fig. 6, 7 and 8. Similarly, it is observed from Fig. 6 that considering non-ideal practical channel estimation with both NBI and TIN, the proposed two schemes...
can achieve more than 4 dB and 2 dB gain over the block and random interleaving schemes, respectively, at the BER of $10^{-4}$ under the PLC multi-path fading channel in 16QAM and 64QAM. It is also noted that with ideal channel knowledge, the proposed interleaving scheme has a slightly larger gain over its counterparts compared to the case with non-ideal practical channel estimation at the receiver. However, with non-ideal channel estimation, the proposed optimization criteria still work well and significantly outperforms its counterparts. It can also be noted from Fig. 7 that the proposed scheme outperforms block interleaving by more than 3dB and 4dB in 16QAM and 64QAM at the target BER of $10^{-4}$, which indicates that the proposed scheme is capable of mitigating NBI impacts under frequency-selective fading channels. The anti-TIN capability can be also verified through Fig. 8, where the BER performance of the proposed scheme outperforms block interleaving by more than 2.4dB and 3.7dB in 16QAM and 64QAM at the BER of $10^{-4}$.

The simulation results are all consistent with the previous theoretical analysis and the quantitative analysis of the merit factors $\xi_T$ and $\xi_F$ in Table I and the duo-distances $d_1$ and $d_2$ in Table II, which proves the validity of the proposed two criteria and the effectiveness and robustness of the optimal interleaving scheme designed based on them.

**VI. CONCLUSION**

In this paper, an optimized time-frequency interleaving scheme is proposed, which employs the block size optimization to improve the anti-TIN capability, and a simple row shifting in sub-matrix operation to optimize the performance in the presence of NBI. Two criteria to maximize the time and frequency diversity are set up in the optimization process. Through both quantitative analysis and simulation results, it is derived that the proposed schemes can achieve better anti-NBI and anti-TIN capabilities than the conventional scheme with shorter interleaving delay and less complexity. The proposed scheme is expected to provide a simple and efficient interleaving method for OFDM-based PLC systems to combat against NBI and TIN in practice, and is theoretically applicable to other channel environments with NBI and/or TIN.

**REFERENCES**


